

FIG. 1

2018

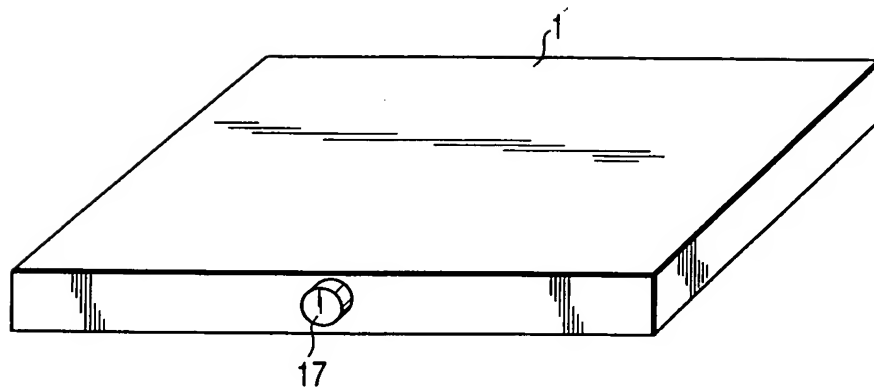


FIG. 2

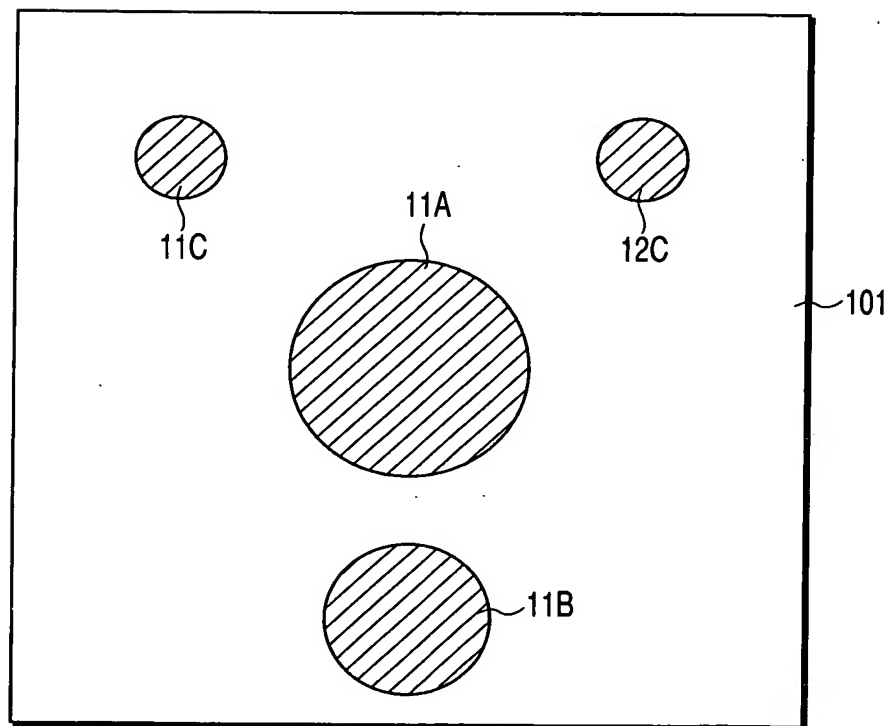


FIG. 3

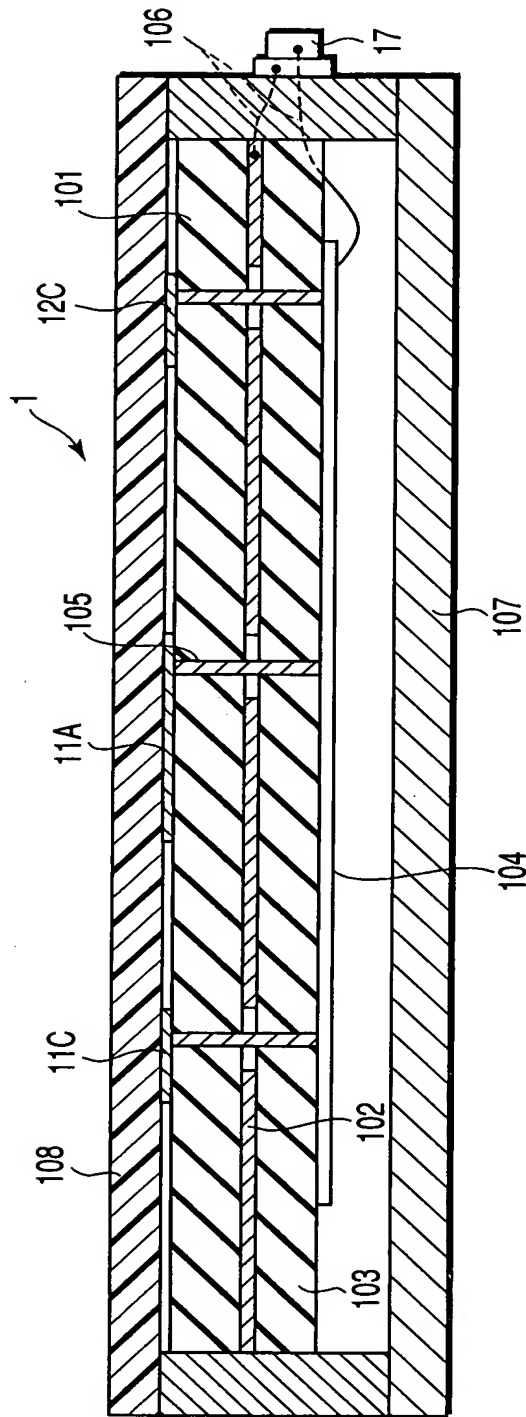


FIG. 4

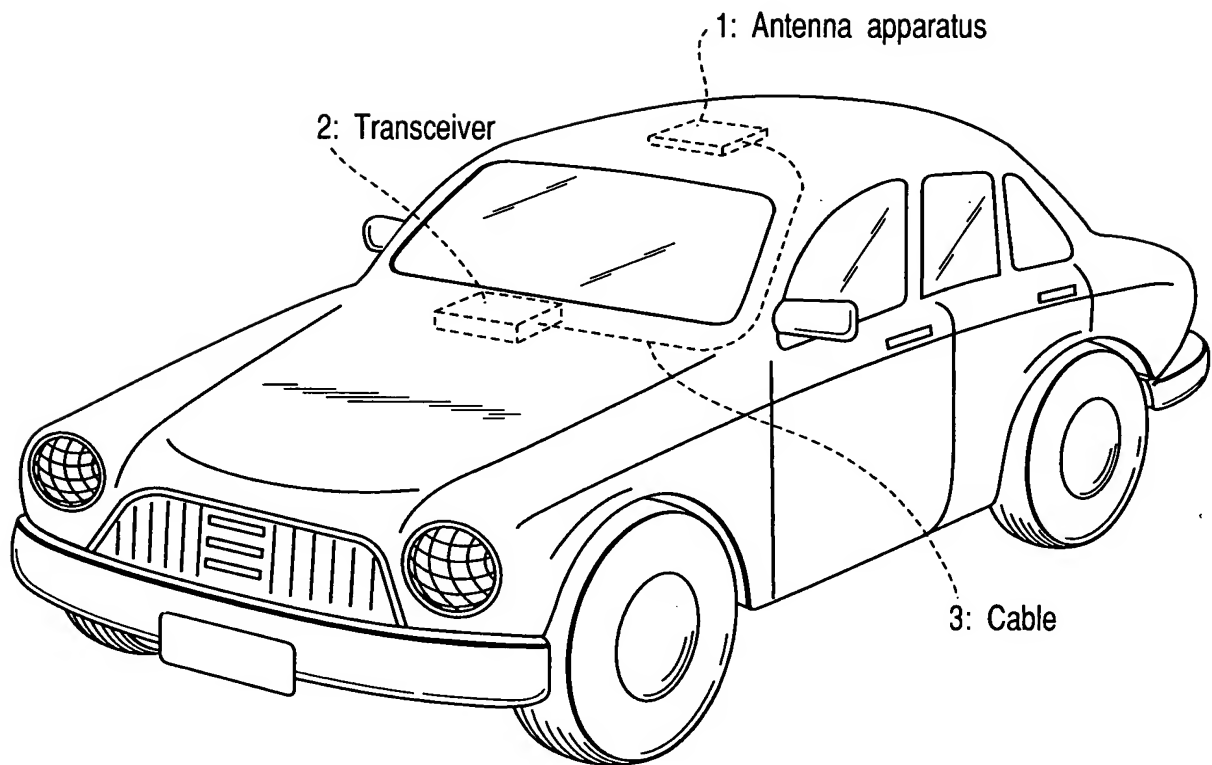
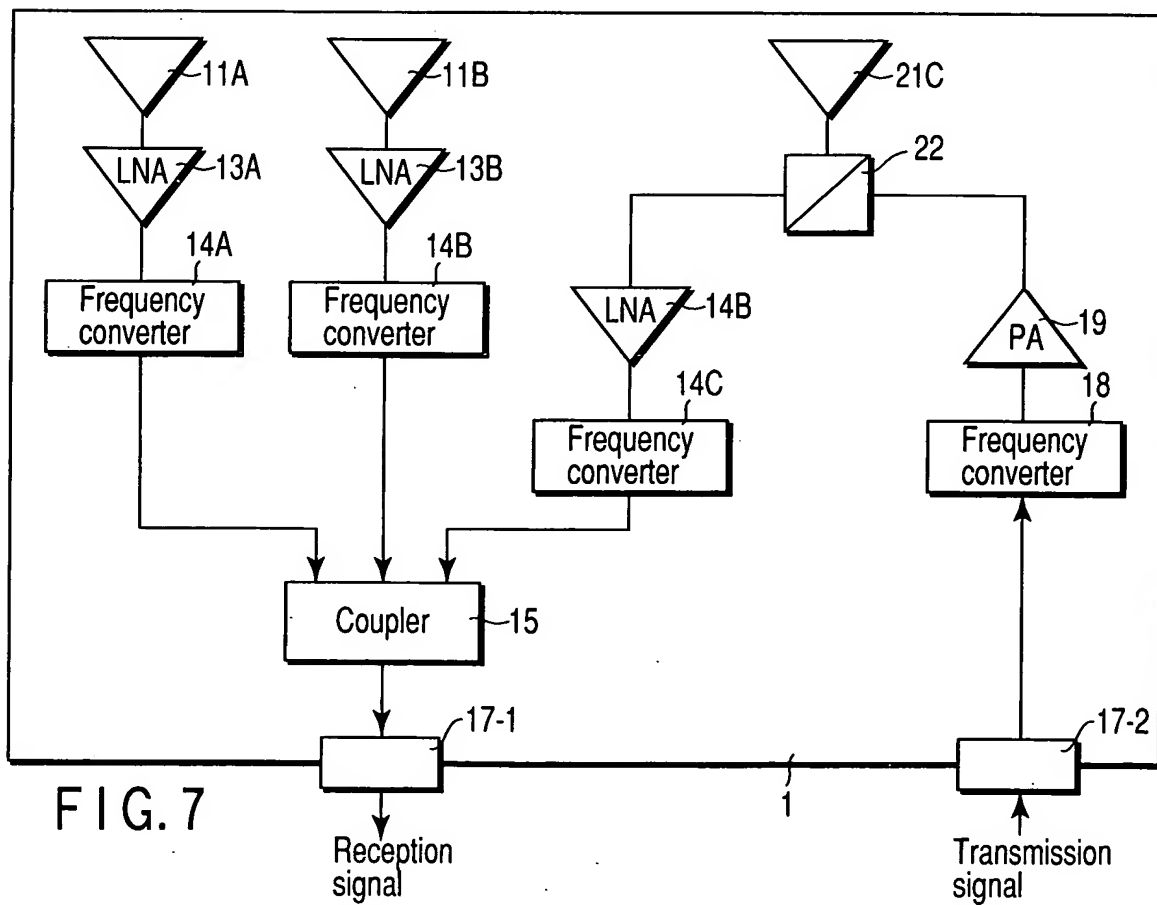
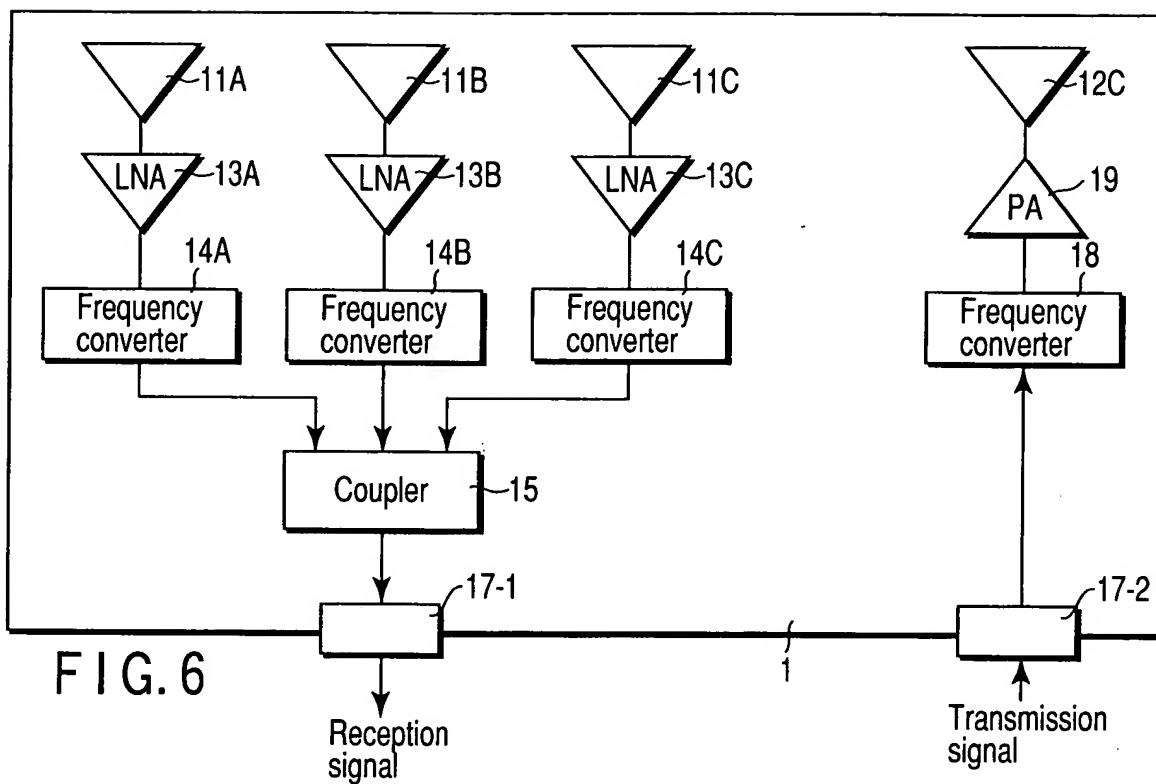
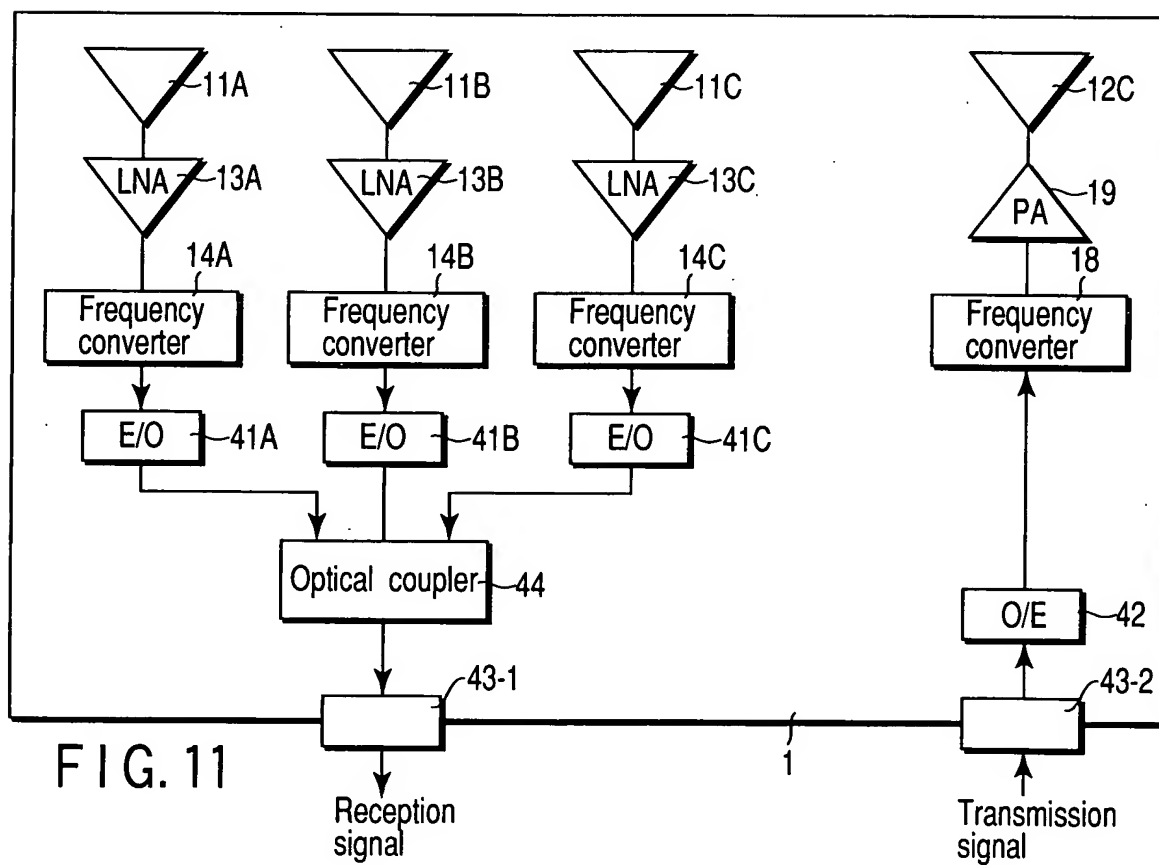
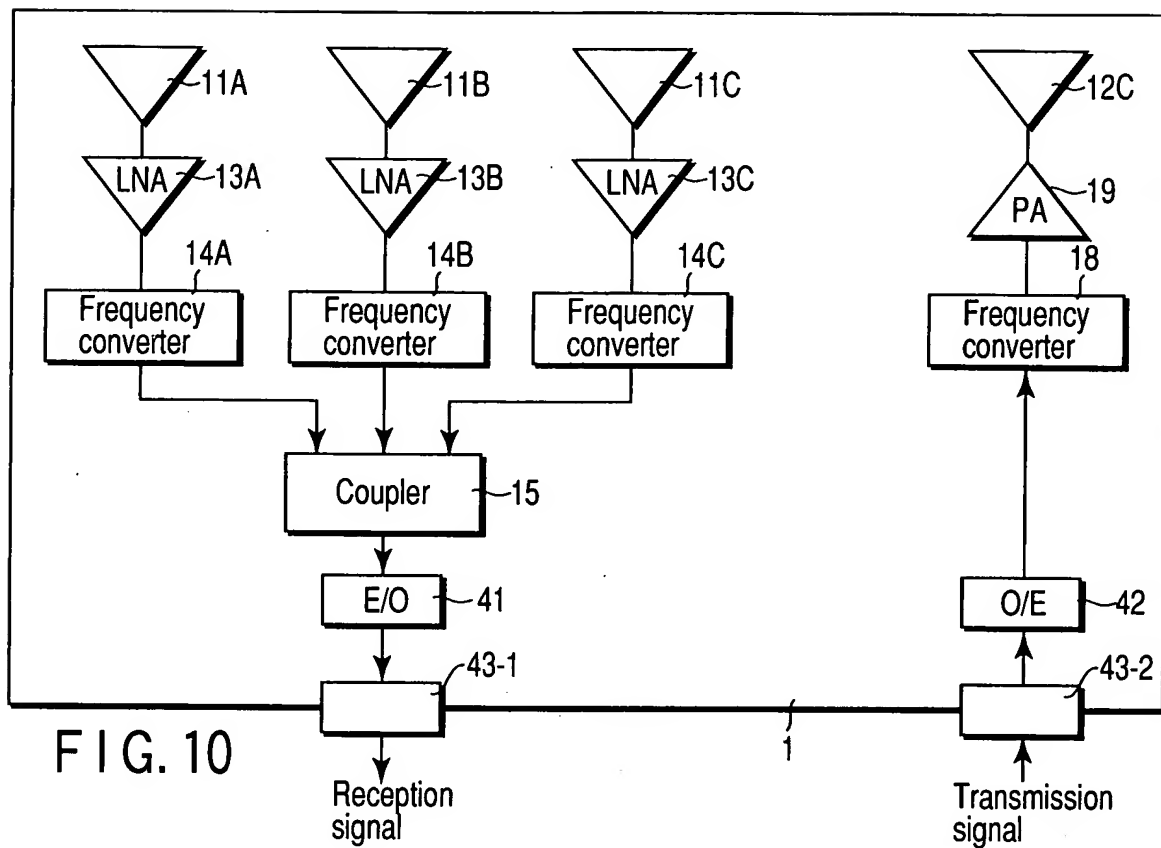


FIG. 5







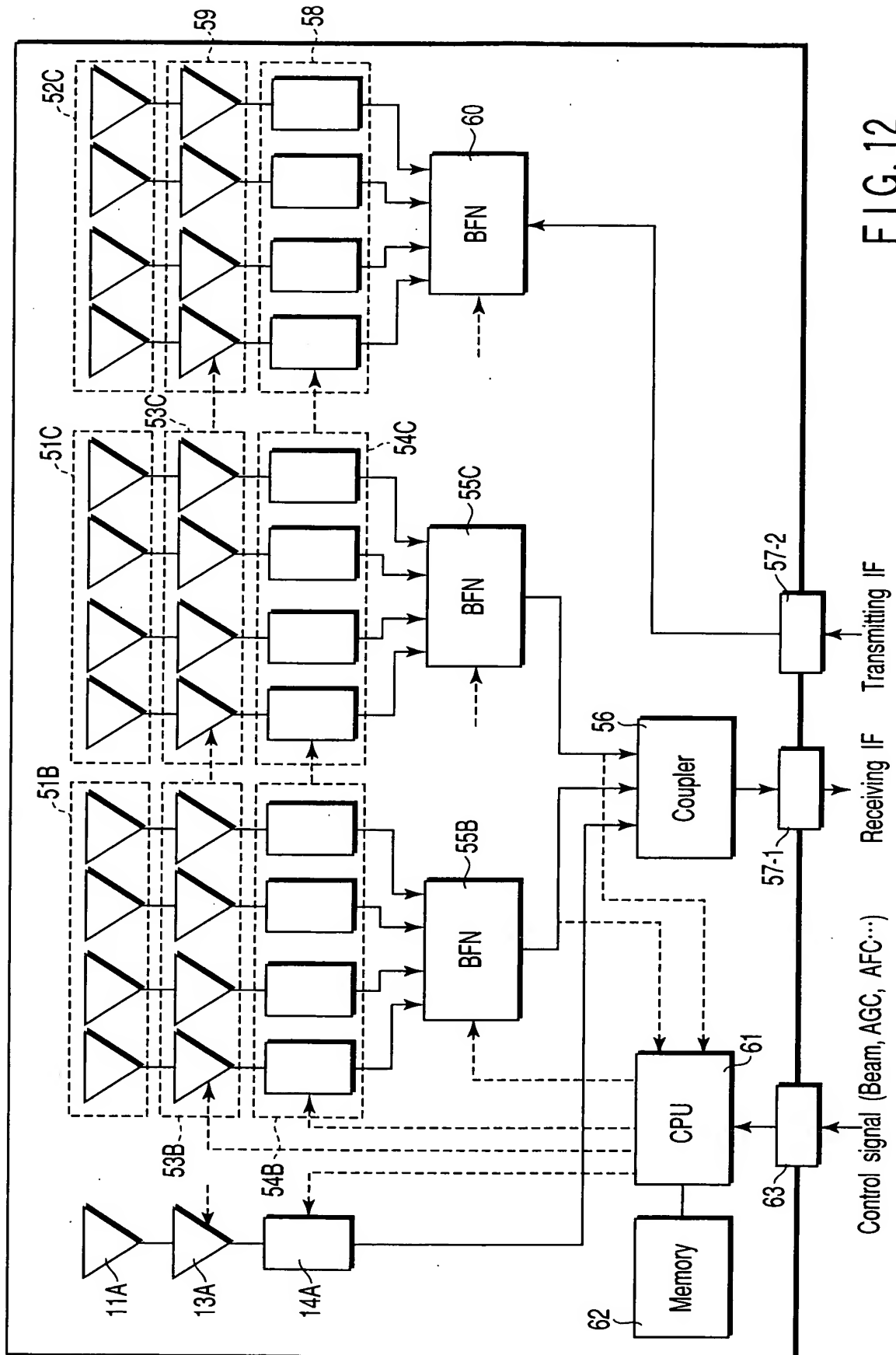
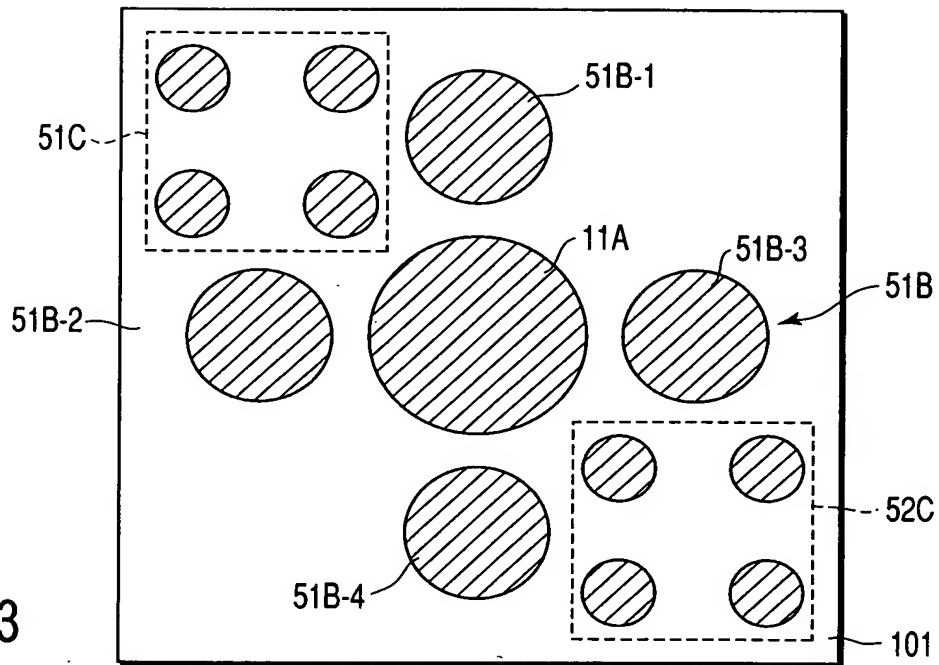


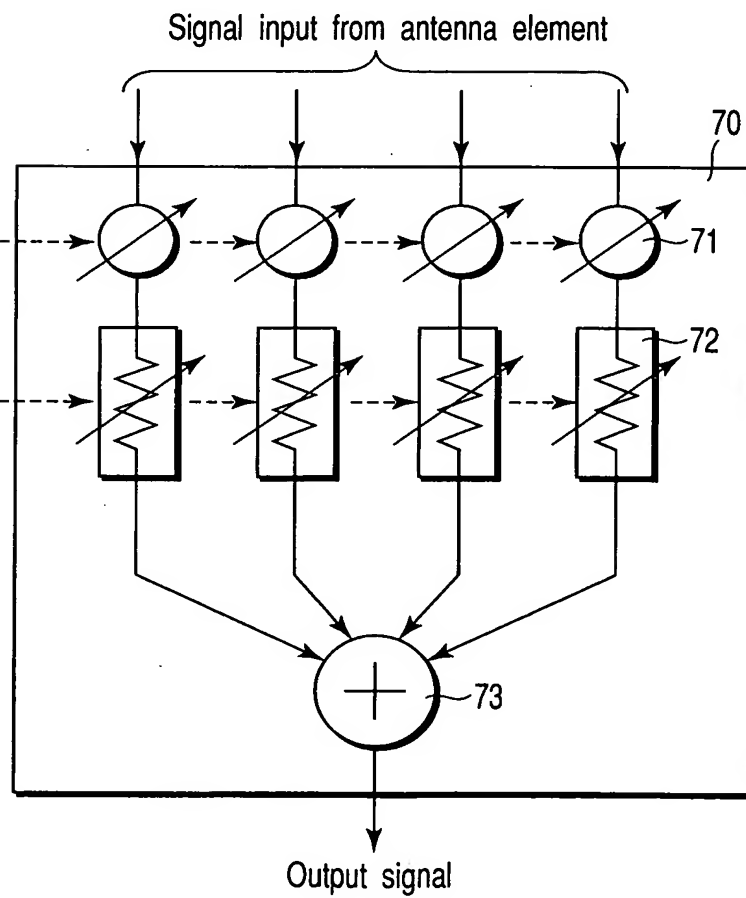
FIG. 12

FIG. 13



Control signal
sent form CPU

FIG. 14



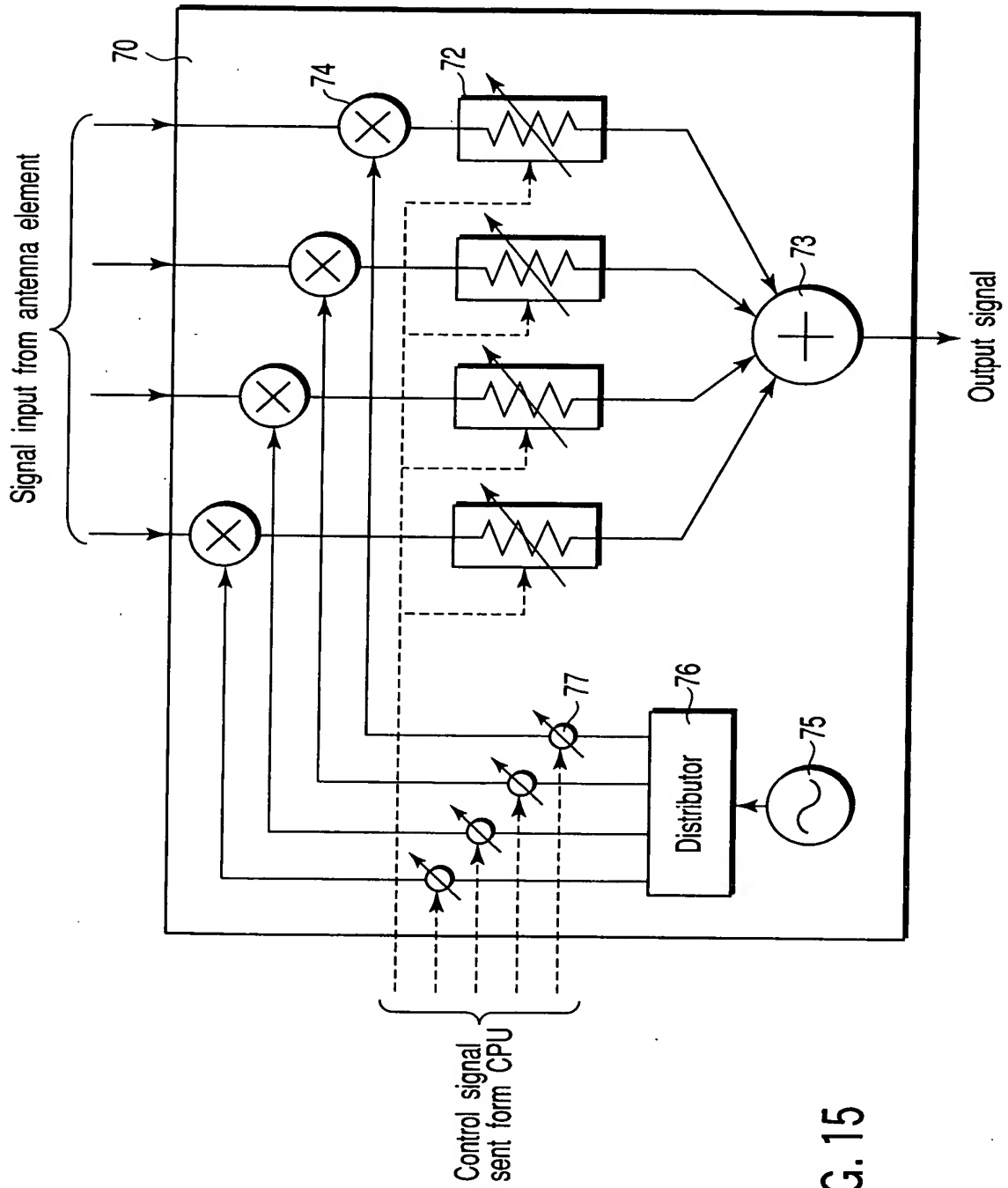


FIG. 15

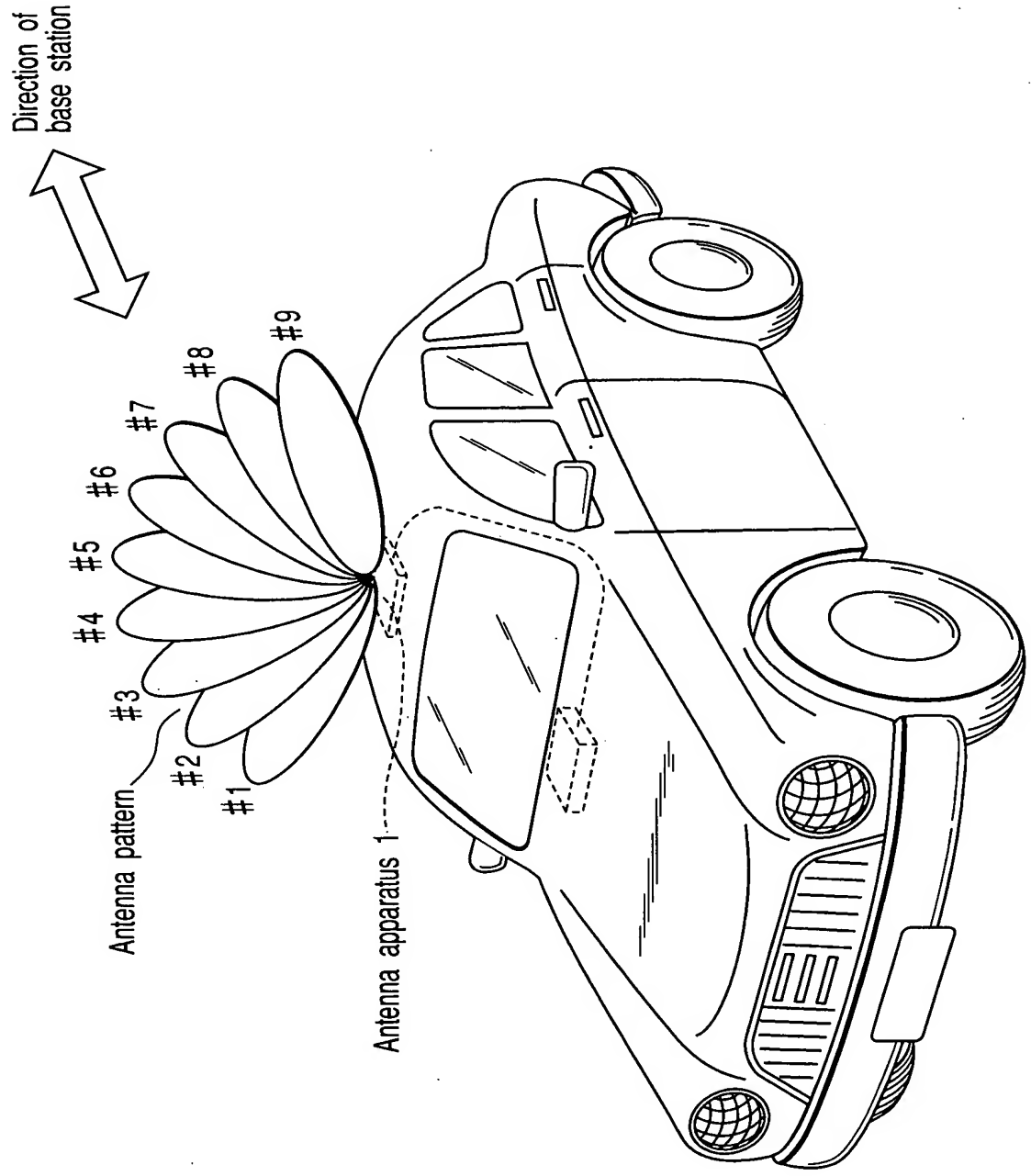


FIG. 16

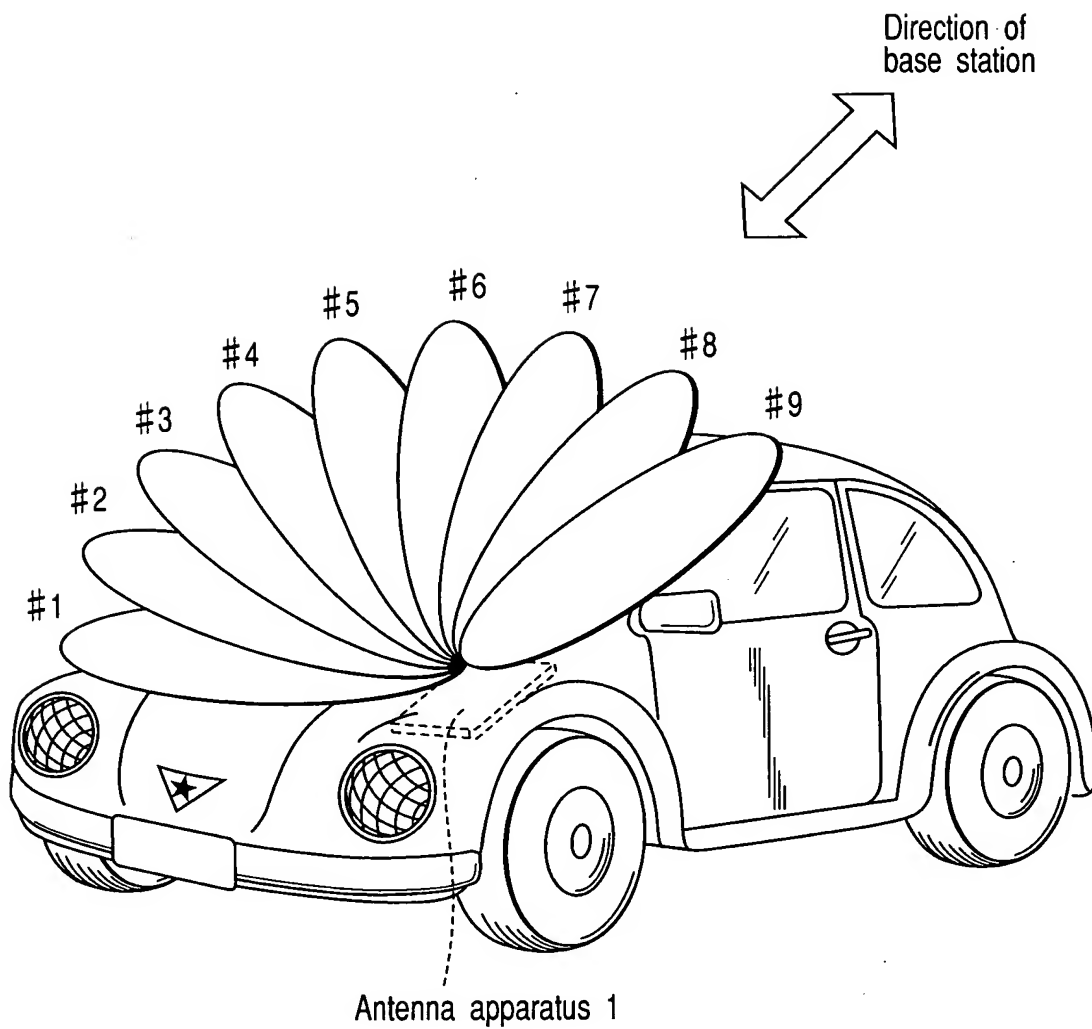


FIG. 17

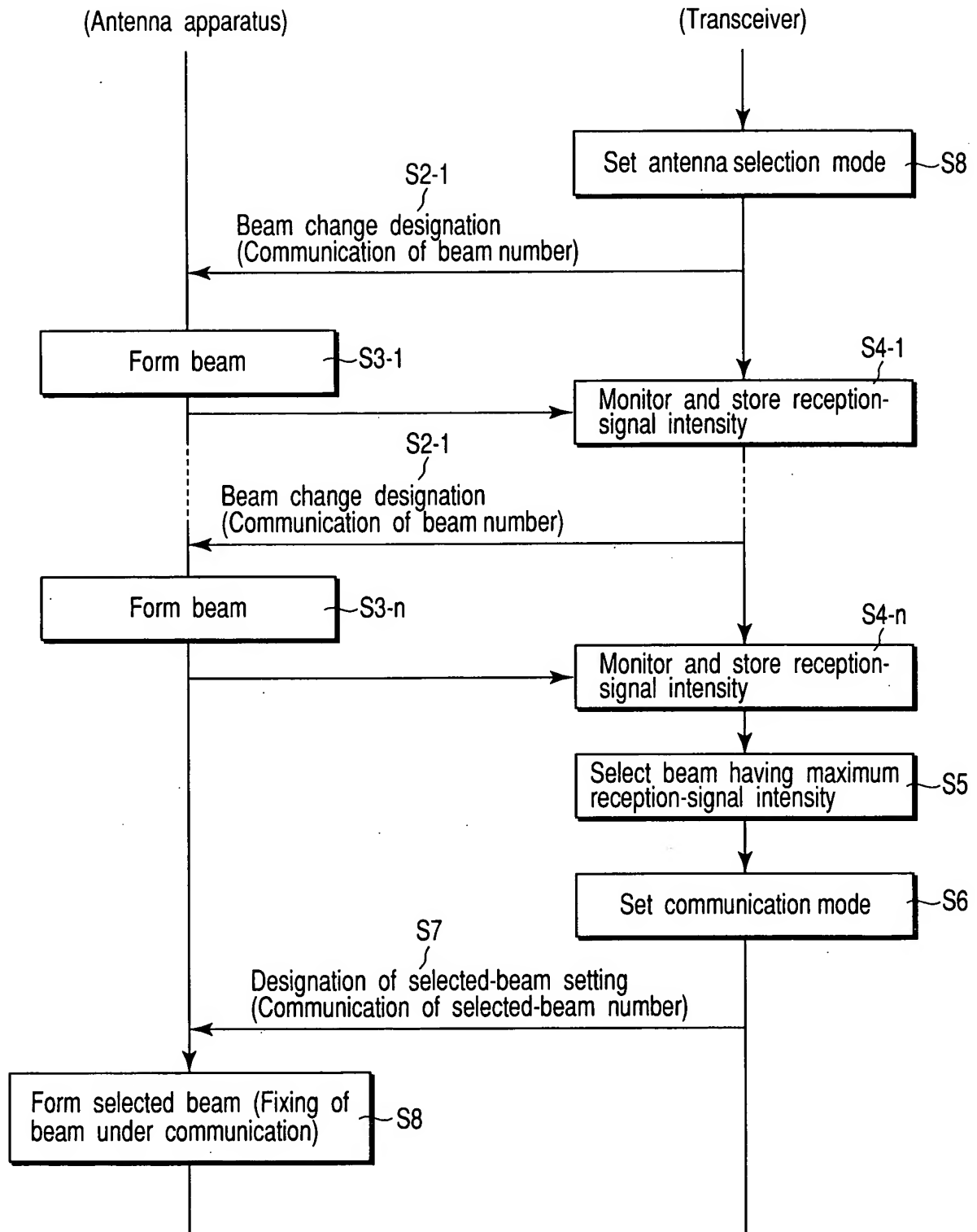


FIG. 18



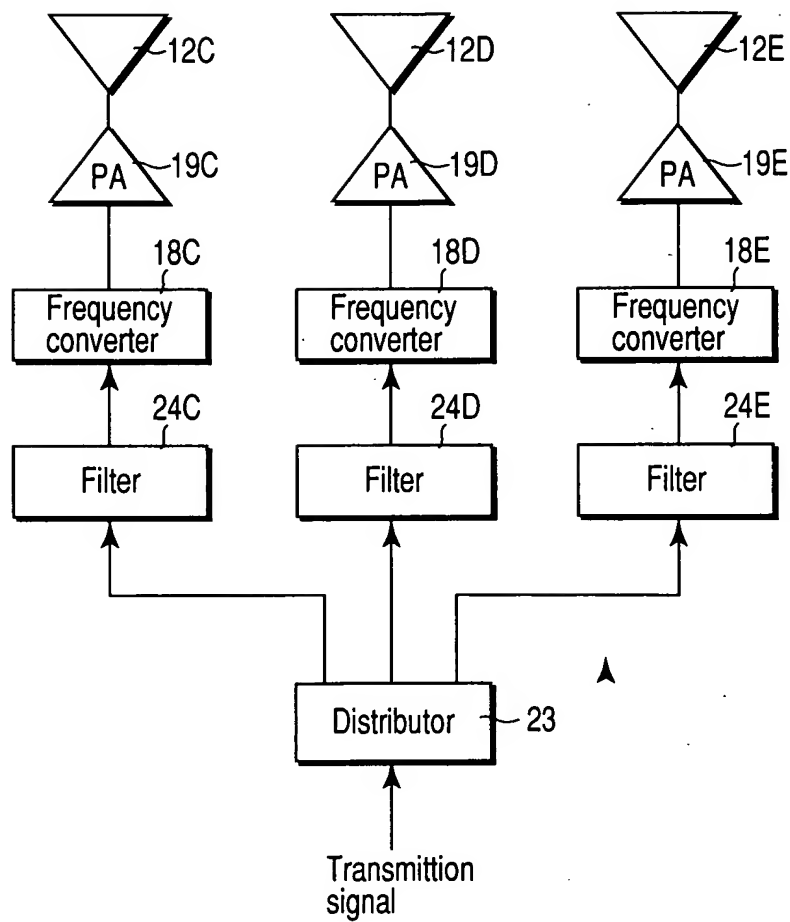


FIG. 20